

L Number	Hits	Search Text	DB	Time stamp
1	734	345/100.ccls.	USPAT	2004/05/30 06:39
2	254	345/95.ccls.	USPAT	2004/05/30 06:39
3	943	345/100.ccls. 345/95.ccls.	USPAT	2004/05/30 06:39
4	573	(345/100.ccls. 345/95.ccls.) and ((shift adj register) or shifter)	USPAT	2004/05/30 06:40
5	170	((345/100.ccls. 345/95.ccls.) and ((shift adj register) or shifter)) and set and reset	USPAT	2004/05/30 06:45
-	1	"6073357"	USPAT	2004/05/29 11:00
-	477	377/47.ccls.	USPAT	2004/05/27 08:36
-	55	377/104.ccls.	USPAT	2004/05/27 08:36
-	84	377/106.ccls.	USPAT	2004/05/27 08:36
-	171	377/107.ccls.	USPAT	2004/05/27 08:37
-	300	377/104.ccls. 377/106.ccls. 377/107.ccls.	USPAT	2004/05/27 08:43
-	47272	counter and multiplex\$3	USPAT	2004/05/27 08:43
-	34827	(counter and multiplex\$3) and clock	USPAT	2004/05/27 08:43
-	20135	((counter and multiplex\$3) and clock) and inver\$4	USPAT	2004/05/27 08:44
-	14534	((((counter and multiplex\$3) and clock) and inver\$4) and (flip-flop or latch)	USPAT	2004/05/27 08:44
-	11781	((((counter and multiplex\$3) and clock) and inver\$4) and (flip-flop or latch)) and reset	USPAT	2004/05/27 08:45
-	291	(((((counter and multiplex\$3) and clock) and inver\$4) and (flip-flop or latch)) and reset) and 377/\$.ccls.	USPAT	2004/05/27 08:46
-	262	(((((counter and multiplex\$3) and clock) and inver\$4) and (flip-flop or latch)) and reset) and 377/\$.ccls.) not 377/47.ccls.	USPAT	2004/05/27 08:46
-	254	(((((counter and multiplex\$3) and clock) and inver\$4) and (flip-flop or latch)) and reset) and 377/\$.ccls.) not 377/47.ccls.) not (377/104.ccls. 377/106.ccls. 377/107.ccls.)	USPAT	2004/05/27 08:47
-	0	10406063.pn.	USPAT	2004/05/28 14:04
-	1	5434899.pn.	USPAT	2004/05/28 14:08
-	201	377/78.ccls.	USPAT	2004/05/28 14:11
-	232	377/79.ccls.	USPAT	2004/05/28 14:11
-	201	377/81.ccls.	USPAT	2004/05/28 14:11
-	571	377/78.ccls. 377/79.ccls. 377/81.ccls.	USPAT	2004/05/28 14:36
-	78315	(shift adj register) or shifter	USPAT	2004/05/28 14:39
-	46564	((shift adj register) or shifter) and clock	USPAT	2004/05/28 14:36
-	12232	((shift adj register) or shifter) and clock) and capacitor	USPAT	2004/05/28 14:37
-	7800	((((shift adj register) or shifter) and clock) and capacitor) and invert\$2	USPAT	2004/05/28 14:37
-	677	(((((shift adj register) or shifter) and clock) and capacitor) and invert\$2) and (thin adj film)	USPAT	2004/05/28 14:38

-	411	(((((shift adj register) or shifter) and clock) and capacitor) and invert\$2) and (thin adj film)) and (MOS or NMOS or N-FET or n-type)	USPAT	2004/05/28 14:39
-	12	((shift adj register) or shifter).ti. and ((((((shift adj register) or shifter) and clock) and capacitor) and invert\$2) and (thin adj film)) and (MOS or NMOS or <del>N-FET or n-type</del> ))	USPAT	2004/05/28 14:40
-	201	377/78.ccls.	USPAT	2004/05/29 11:02
-	232	377/79.ccls.	USPAT	2004/05/29 11:03
-	416	377/78.ccls. 377/79.ccls.	USPAT	2004/05/29 11:13
-	78315	(shift adj register) or shifter	USPAT	2004/05/29 11:14
-	28777	((shift adj register) or shifter) and set and reset	USPAT	2004/05/29 11:14
-	2463	((shift adj register) or shifter) and set and reset) and clock and refresh	USPAT	2004/05/29 11:14
-	2356	((shift adj register) or shifter).ti.	USPAT	2004/05/30 06:38
-	16	((shift adj register) or shifter).ti.) and (((shift adj register) or shifter) and set and reset) and clock and refresh)	USPAT	2004/05/29 11:15